

[illegible]

FIG. 2

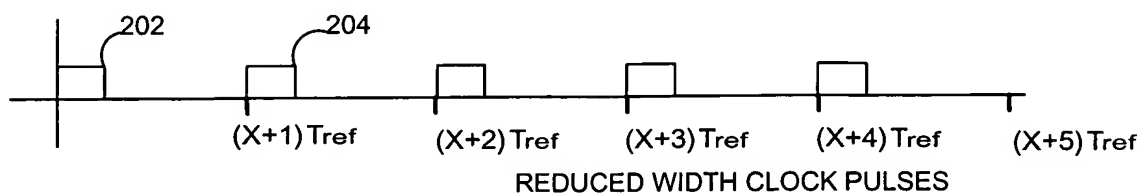


FIG. 3

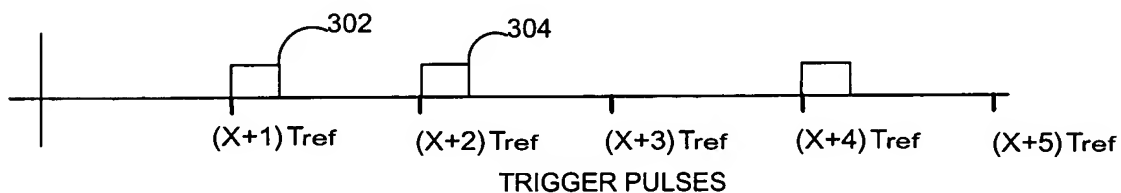


FIG. 4

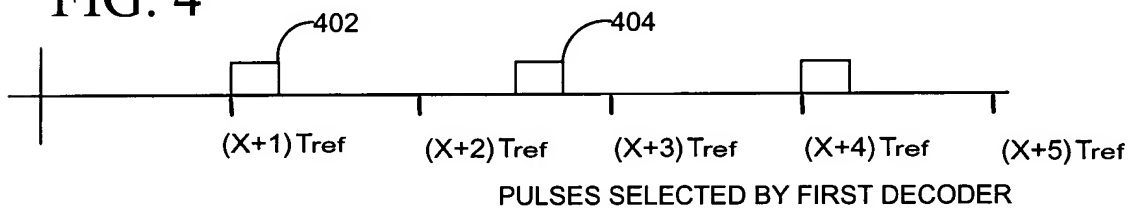


FIG. 5

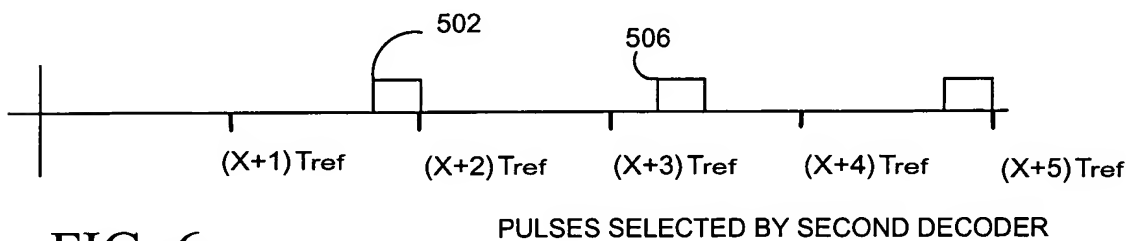
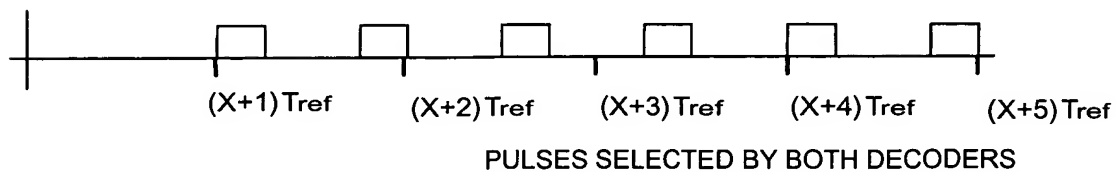
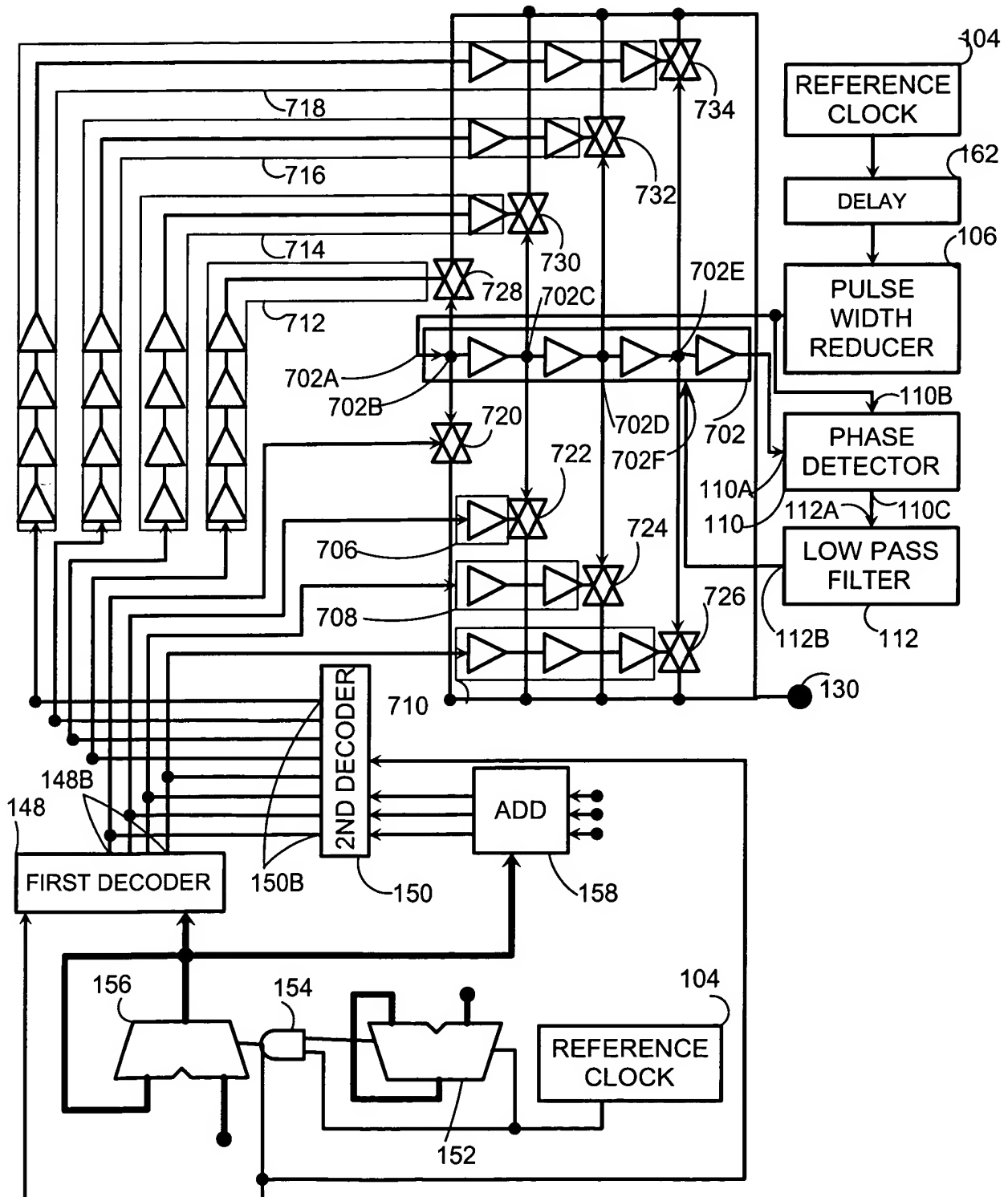


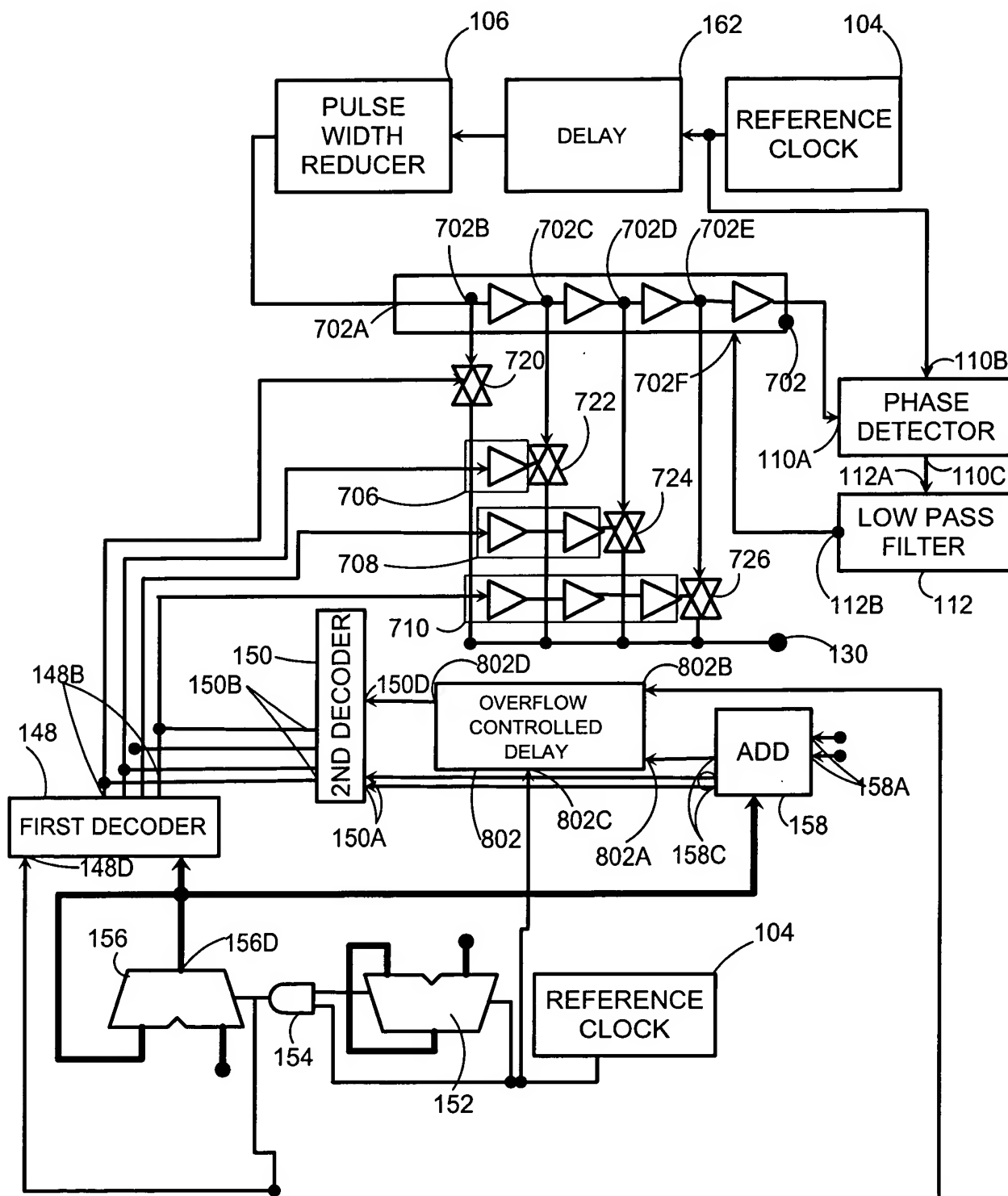
FIG. 6



700



800



The diagram shows four signals over time:

- $V_{IN}(t)$: Input voltage, a step function that transitions from a low level to a high level and back to low.
- $V_A(t)$: Intermediate signal, a step function that transitions from a low level to a high level and back to low, occurring at the same time as $V_{IN}(t)$.
- $V_B(t)$: Intermediate signal, a step function that transitions from a low level to a high level and back to low, occurring at the same time as $V_{IN}(t)$.
- $V_{OUT}(t)$: Output voltage, a step function that transitions from a low level to a high level and back to low, occurring at a later time than $V_{IN}(t)$.

The delay between the input signal $V_{IN}(t)$ and the output signal $V_{OUT}(t)$ is labeled δ .

FIG. 12

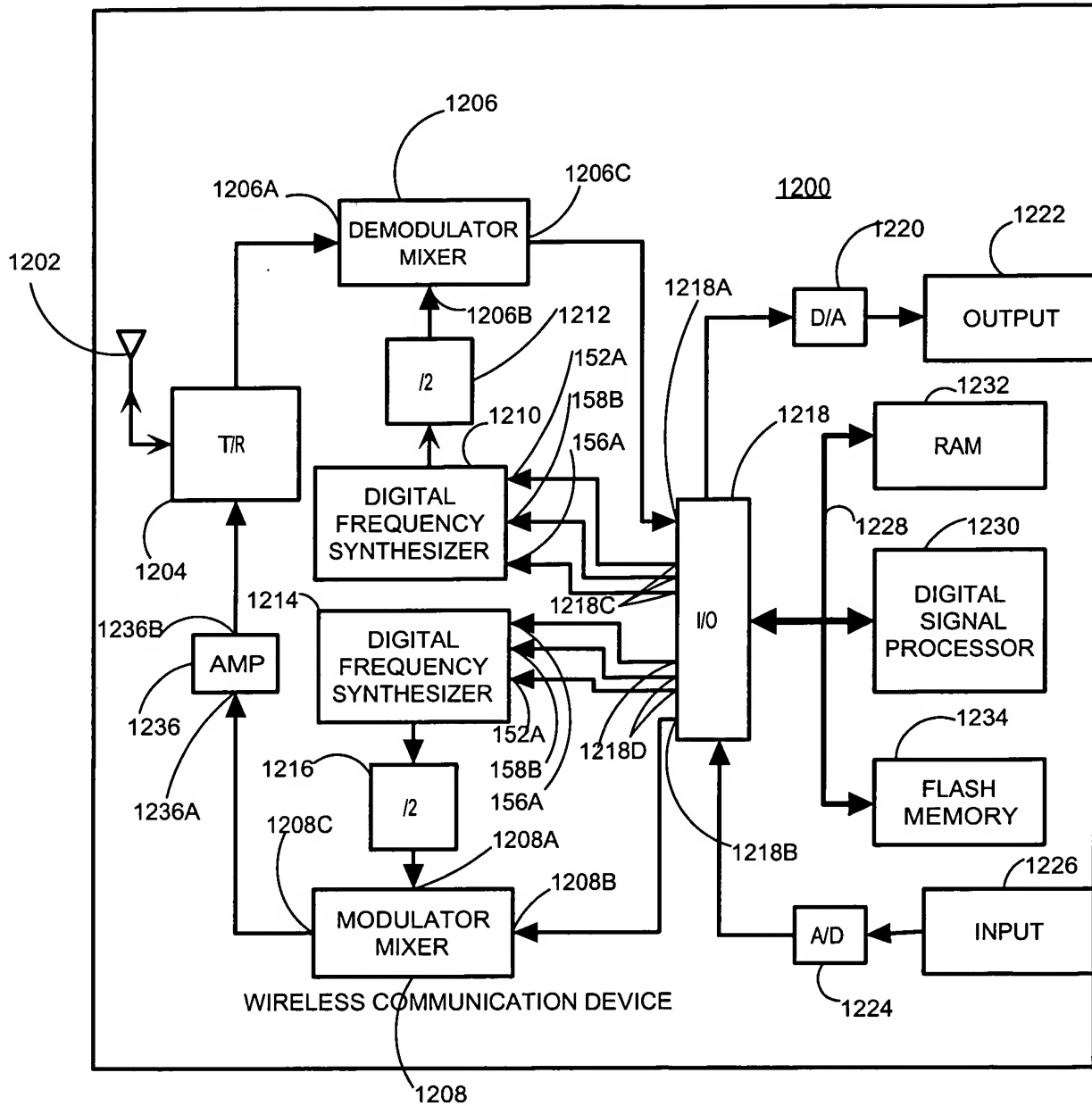


FIG. 13

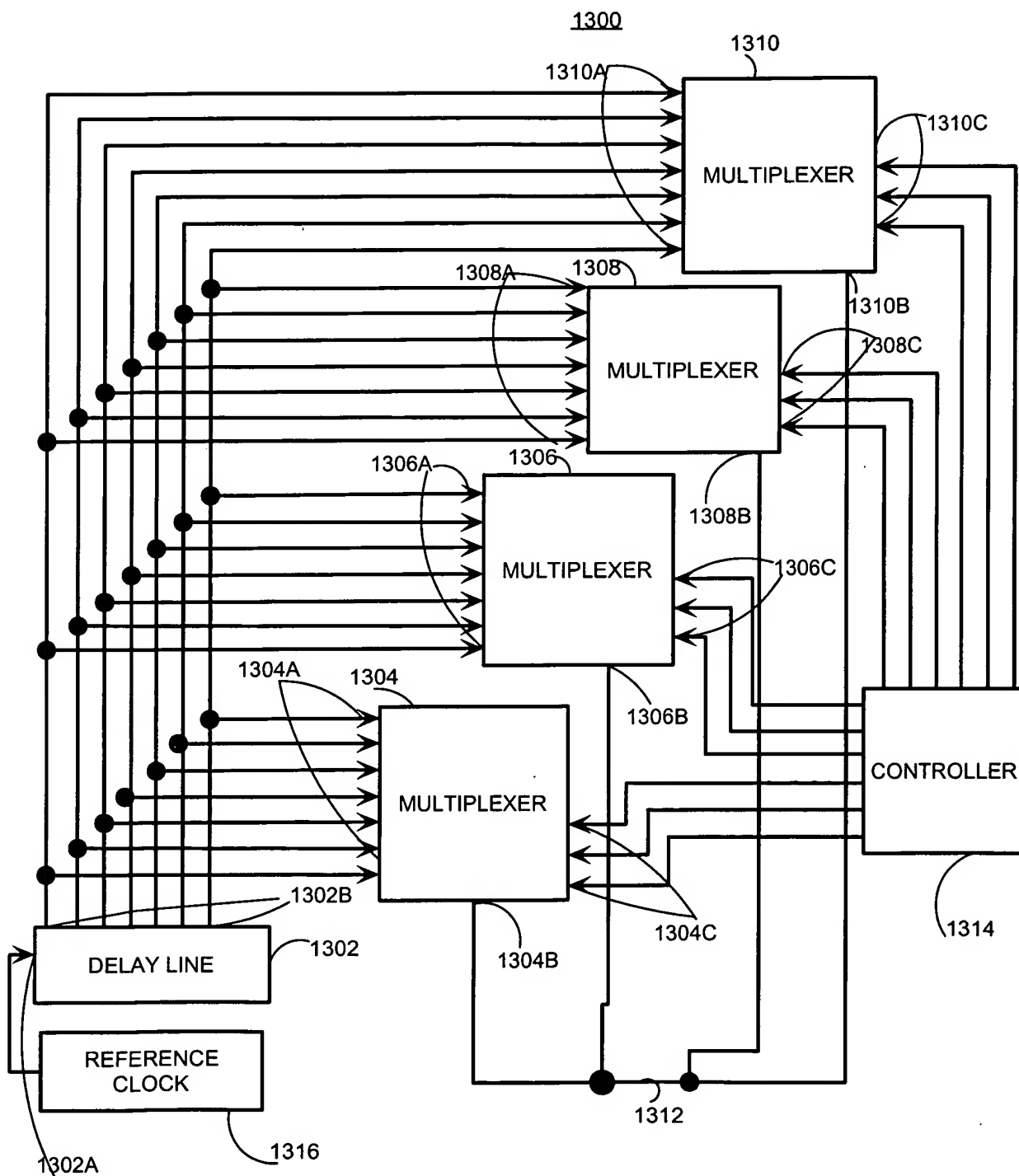


FIG. 14

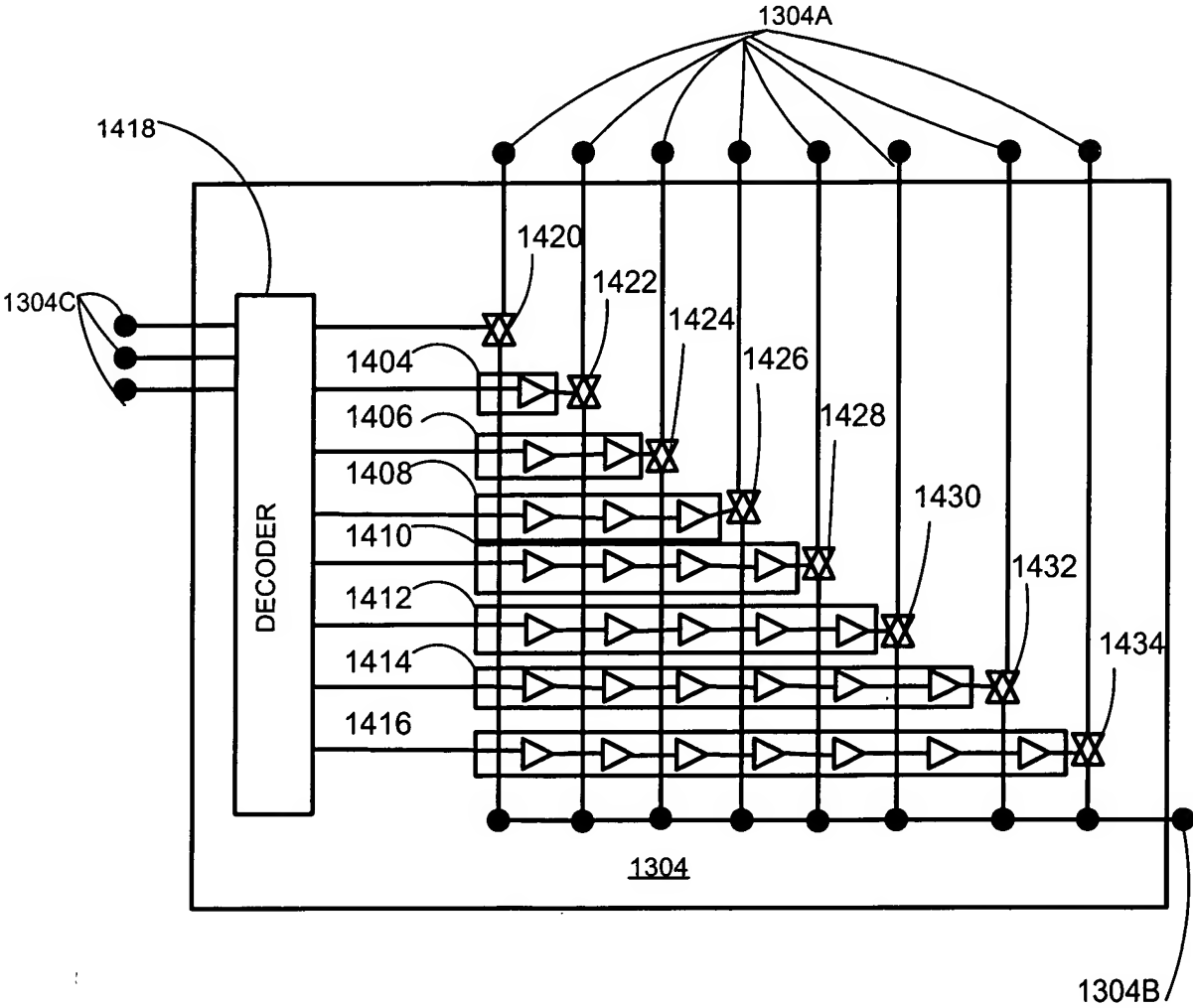


FIG. 15

